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FIG.1

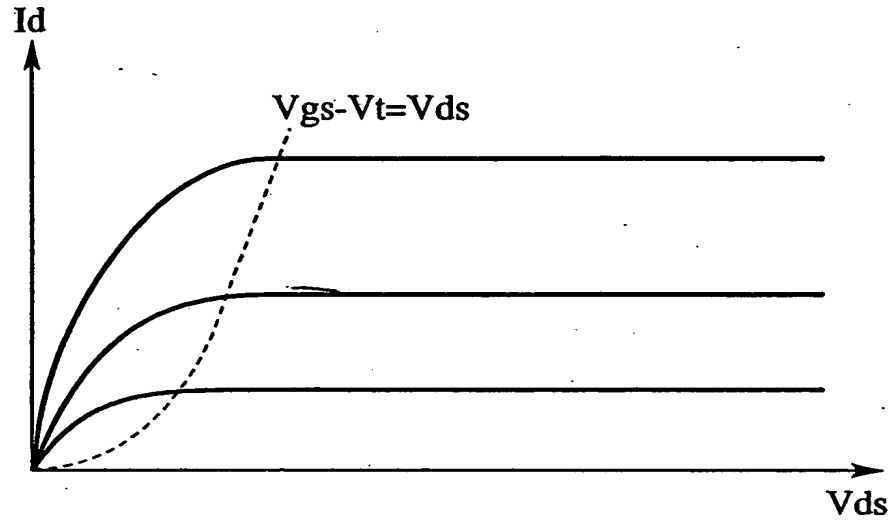
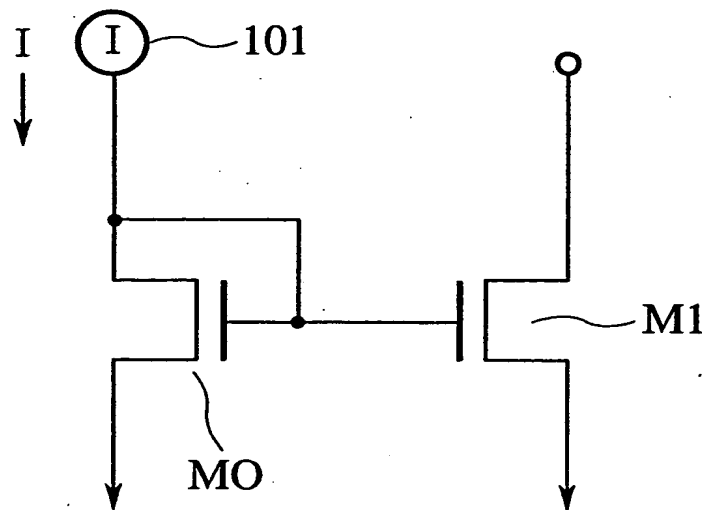


FIG.2



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FIG.3

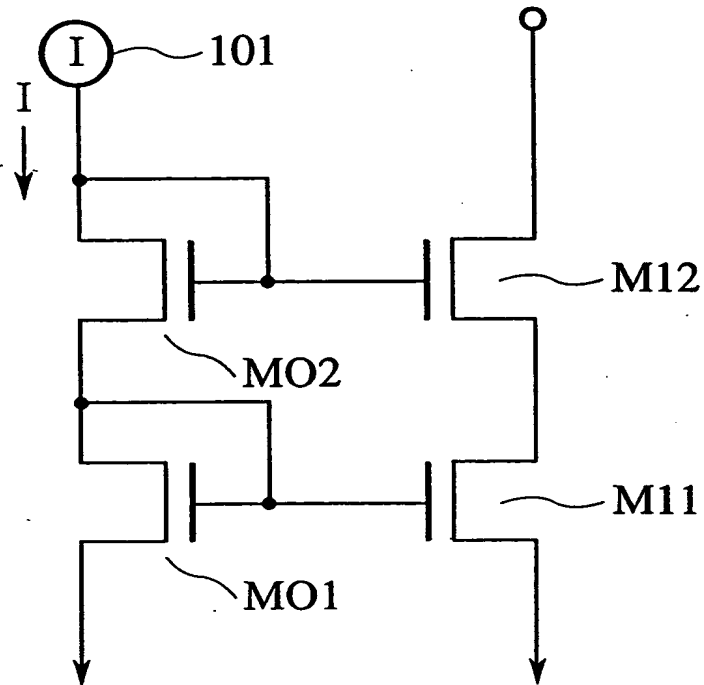
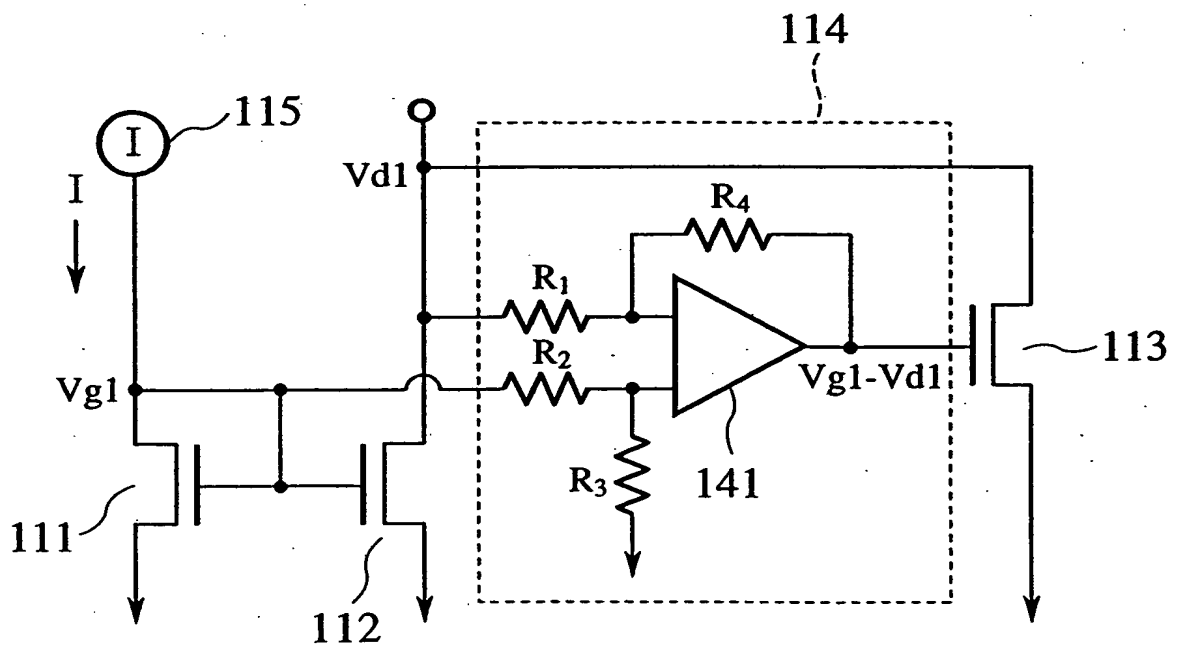


FIG.4



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FIG.5

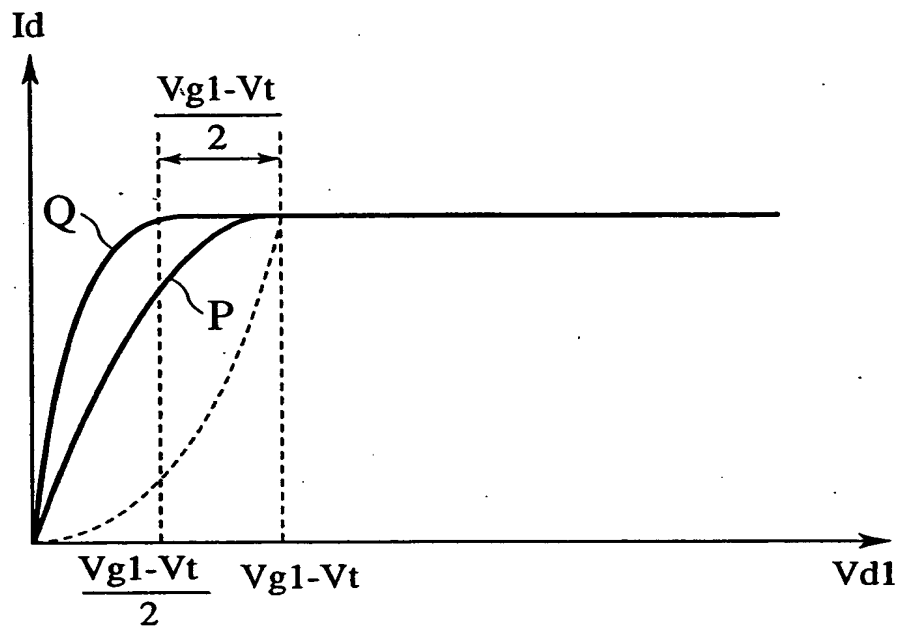
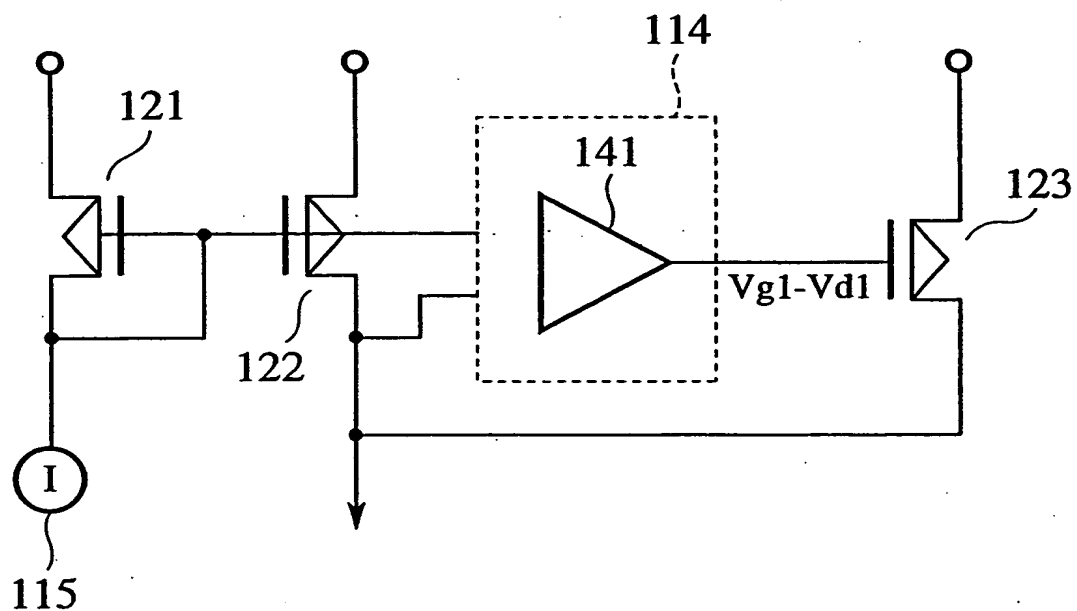


FIG.6



[illegible][illegible]

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FIG. 9

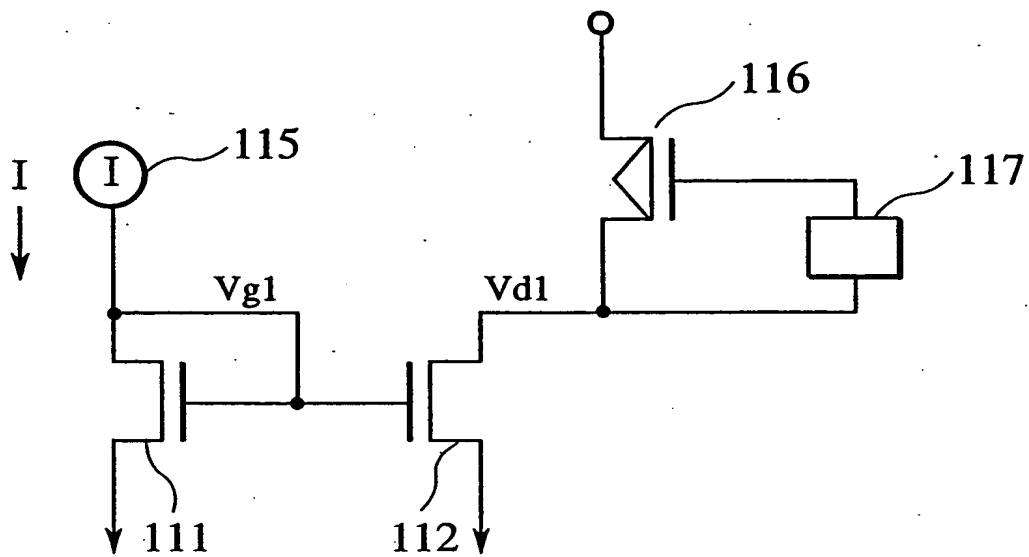
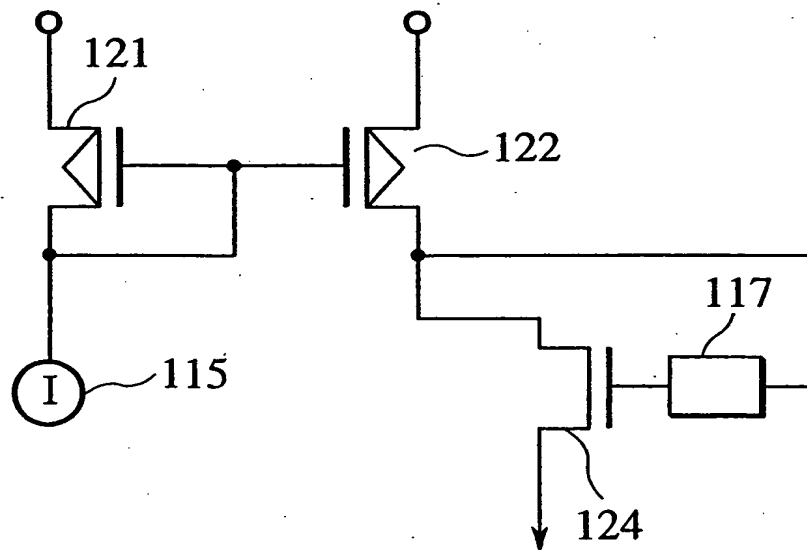


FIG. 10



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FIG.11

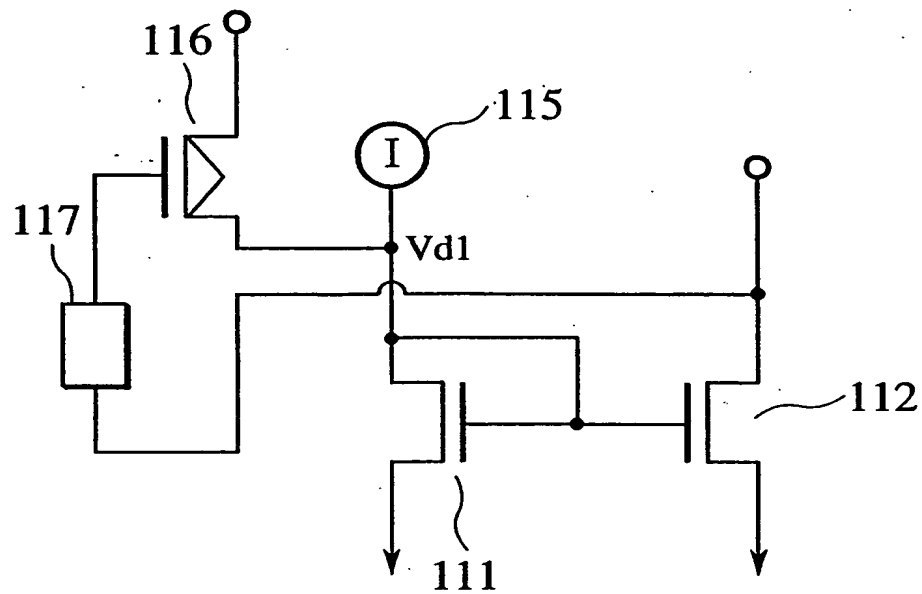
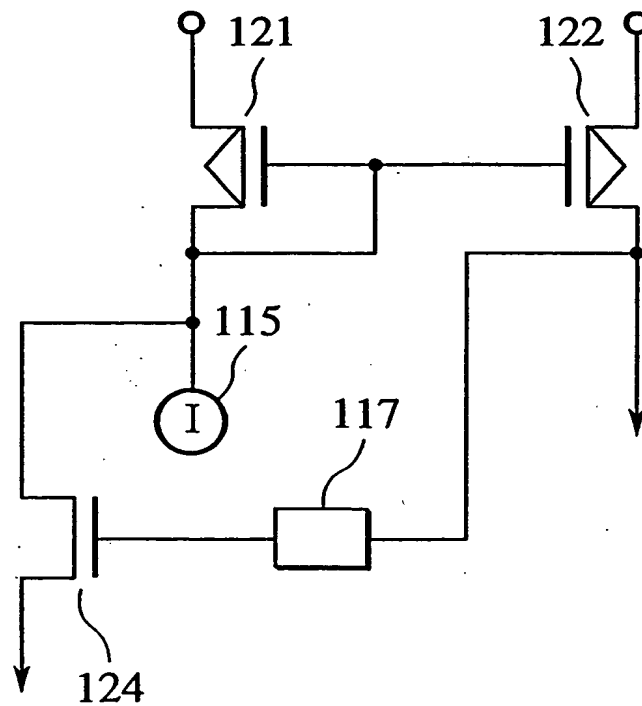


FIG.12



The circuit diagram shows a differential amplifier with two input branches. The top branch consists of a PMOS transistor 111 and an NMOS transistor 112. The bottom branch consists of a PMOS transistor 118 and an NMOS transistor 119. The gates of transistors 111 and 118 are connected to a common gate voltage  $V_{g1}$ . The gates of transistors 112 and 119 are connected to a common gate voltage  $V_{g2}$ . The drains of transistors 111 and 118 are connected to a common drain voltage  $V_{d1}$ . The drains of transistors 112 and 119 are connected to a common drain voltage  $V_{d2}$ . The sources of transistors 112 and 119 are connected to a common source voltage  $V_{s1}$ . The sources of transistors 111 and 118 are connected to a common source voltage  $V_{s2}$ . The gates of transistors 112 and 119 are connected to the drains of transistors 111 and 118, respectively, through feedback paths 131 and 132. The gates of transistors 111 and 118 are connected to the drains of transistors 112 and 119, respectively, through feedback paths 133 and 134. The gates of transistors 112 and 119 are connected to the drains of transistors 111 and 118, respectively, through feedback paths 131 and 132. The gates of transistors 111 and 118 are connected to the drains of transistors 112 and 119, respectively, through feedback paths 133 and 134.



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FIG.15

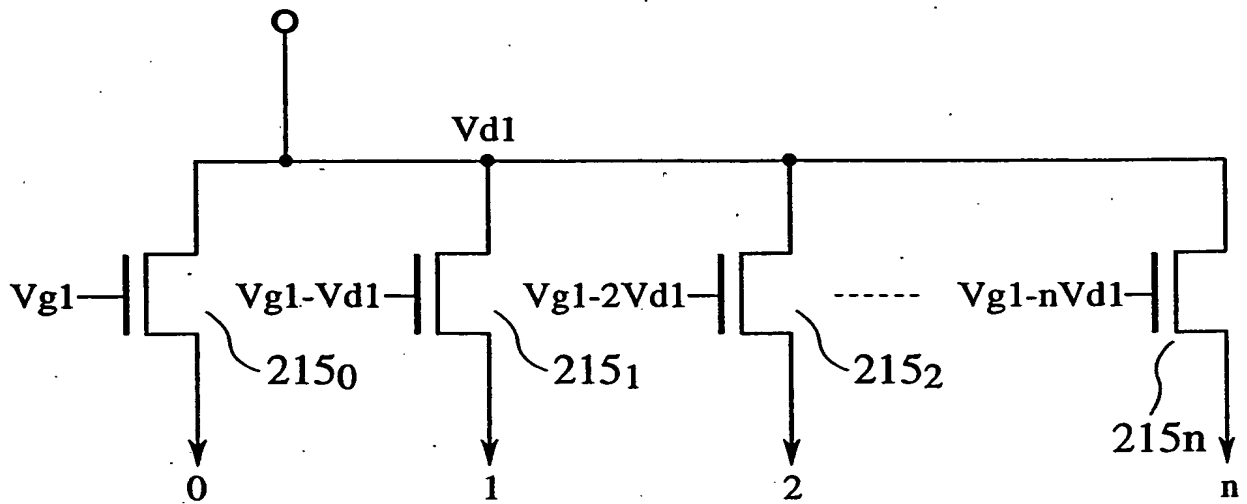
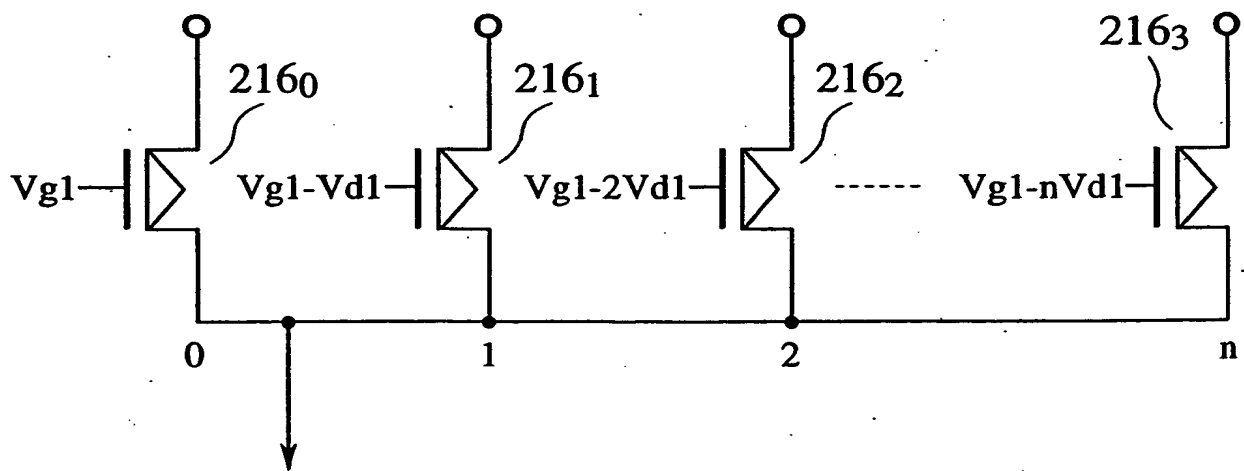


FIG.16



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FIG. 17

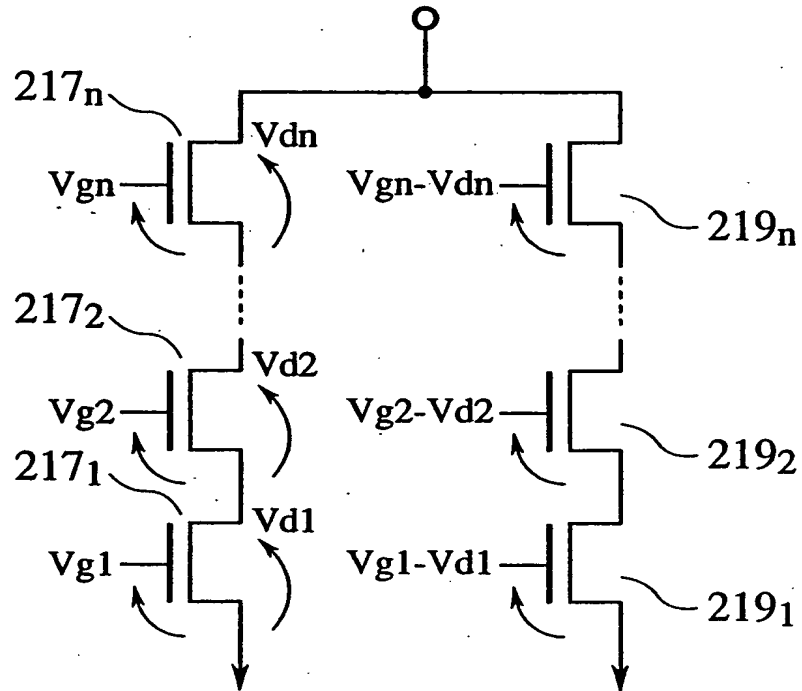


FIG. 18

